

IN THE CLAIMS

Please amend the claims as follows:

1. (Original) A method for performing virtualization, comprising:
executing a plurality of input output (IO) instructions from an instruction stream during a signal virtualization event.
2. (Original) The method of Claim 1, further comprising:
identifying an IO instruction; and
scanning the instruction stream to determine whether additional IO instructions are present within an extent of instructions in the instruction stream.
3. (Original) The method of Claim 2, further comprising identifying the plurality of IO instructions in a block of instructions within the extent.
4. (Original) The method of Claim 3, further comprising performing a hash function on the block of instructions.
5. (Original) The method of Claim 2, wherein the extent is determined by a processor running in a system.
6. (Original) The method of Claim 2, wherein the extent is determined by hardware in a system.
7. (Original) The method of Claim 2, wherein the extent is determined by software in a system.
8. (Original) The method of Claim 3, wherein executing the plurality of IO instructions comprises emulating the block of instructions.

9. (Original) The method of Claim 8, further comprising updating an instruction pointer to move past the block of instructions.

10-17 (Canceled)

18. (Original) An article of manufacturer comprising a machine accessible medium including sequences of instructions, the sequences of instructions including instructions which when executed causes the machine to perform:

executing a plurality of input (IO) instructions from an instruction stream during a single virtualization event.

19. (Original) The article of manufacturer of Claim 18, further comprising instructions which when executed by the machine causes the machine to perform:

identifying an IO instruction; and

scanning the instruction stream to determine whether additional IO instructions are present within an extent of instructions in the instruction stream.

20. (Original) The article of manufacturer of Claim 19, further comprising instructions which when executed by the machine causes the machine to perform identifying the plurality of IO instructions in a block of instructions within the extent.

21. (Original) The article of manufacture of Claim 20, wherein executing the plurality of IO instructions comprises emulating the block of instructions.

22. (Original) The article of manufacturer of Claim 20, further comprising instructions which when executed by the machine causes the machine to perform updating an instruction pointer to move past the block of instructions.

23-25 (Canceled)

26. (Previously Presented) The article of Claim 20, further comprising instructions which when executed by the machine causes the machine to perform a hash function on the block of instructions.

27. (Previously Presented) The article of manufacturer of claim 19, wherein the extent is determined by a processor running in a system.

28. (Previously Presented) The article of manufacturer of claim 19, wherein the extent is determined by hardware in a system.

29. (Previously Presented) The article of manufacturer of Claim 19, wherein the extent is determined by software in a system.

30. (Previously Presented) A virtual machine manager, comprising:
virtualization event dispatcher to execute a plurality of input output (IO) instructions from an instruction stream during a single virtualization event

31-36 (Canceled)